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Joseph A. Sawyer, Jr.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Date: December 13, 2001

Okuda, et al.

Serial No.: *to be assigned*

Group Art Unit: *to be assigned*

Filed: *herewith*

Examiner: *to be assigned*

For: TABLE OF WAFER POLISHING APPARATUS, METHOD FOR
POLISHING SEMICONDUCTOR WAFER, AND METHOD FOR
MANUFACTURING SEMICONDUCTOR WAFER

Box PCT
Assistant Commissioner of Patents
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Sir:

Please enter the following amendments and remarks into the above-referenced patent application:

IN THE SPECIFICATION

On page 1, line 1 has been amended to read :

Title of the Invention.

On page 1, line 7, has been amended to read:

Background of the Invention.

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